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European Patent Office
Office européen des brevets



⑪ Publication number:

0 596 582 A1

⑫

EUROPEAN PATENT APPLICATION

⑬ Application number: 93203467.1

⑮ Int. Cl. 5: **H01L 21/86, H01L 49/02,
H05K 1/09, H05K 3/32,
H05K 7/02**

⑭ Date of filing: 29.03.89

This application was filed on 10 - 12 - 1993 as a divisional application to the application mentioned under INID code 60.

⑯ Priority: 30.03.88 JP 77225/88

⑰ Date of publication of application:
11.05.94 Bulletin 94/19

⑱ Publication number of the earlier application in accordance with Art.76 EPC: 0 335 679

⑲ Designated Contracting States:
DE FR GB

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㉓ **Bonded ceramic-metal composite substrate and circuit board constructed therewith.**

㉔ A bonded ceramic-metal composite substrate comprising a ceramic substrate (1) having opposite surfaces and a copper sheet (2) having a face directly bonded to one of the surfaces of the ceramic substrate, wherein the copper sheet has a Vickers hardness in the range of 40 kg/mm² to 100 kg/mm². A circuit board comprising the substrate and methods of making the substrate and circuit board are also disclosed.

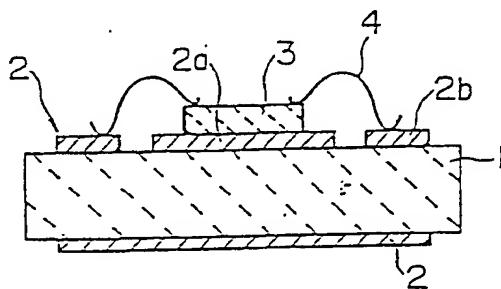


Fig. I

Background of the Invention

This invention relates to a bonded ceramic-metal composite substrate comprising a copper sheet directly bonded to a ceramic substrate, to a circuit board constructed of such a bonded composite, and to methods for their production.

In recent years, attempts have been made to develop bonded ceramic-metal composite substrates for use in electronic devices. For example, a capacitor element bonded to a ceramic aluminum nitride substrate is disclosed in Scace et al., U.S. Patent No. 3,716,759. Direct bonding of metal sheets to ceramic substrates is also described in Hill, GB 2,099,742 and Jochym, U.S. Patent No. 4,409,278, whereby channels are provided in the metal or ceramic substrate in order to avoid blistering by permitting gas escape during the bonding. Some of these attempts have involved disposing a metal sheet such as copper on a ceramic substrate and directly bonding the copper sheet to the substrate by heating at a temperature below the melting point of copper (1083 °C) and above the eutectic point of copper and oxygen (1065 °C).

US-A-3 994 430 discloses a method of directly bonding a metal sheet such as copper to a ceramic substrate by forming a copper-eutectic melt.

This kind of composite has several advantages. It provides a strong adhesion between the conductive metal sheet and the ceramic substrate. It also provides a simpler structure which can be easily utilized to obtain a smaller circuit board. And it provides a potentially simpler method for producing a circuit board.

However, in attempting to use ceramic-copper substrates prepared in the foregoing manner in circuit boards for transistor modules, the present inventors found that some of them did not work correctly and others did not work at all. Thus, there is a need for bonded ceramic-copper substrates which will produce more reliable electronic devices.

Summary of the Invention

This invention was made to solve the foregoing problem. The object of this invention is to provide a bonded ceramic-metal composite substrate comprising a copper sheet directly bonded to a ceramic substrate, a circuit board produced from such a composite substrate which works with a high degree of operational reliability, and methods for production thereof.

This invention is directed to a bonded ceramic-metal composite substrate comprising a ceramic substrate having opposite surfaces and a copper sheet having a face directly bonded to one of the surfaces of the ceramic substrate, wherein the copper sheet has a Vickers hardness in the range of 40 kg/mm² to 100 kg/mm².

According to a further aspect of the invention, a circuit board is provided comprising (a) a bonded ceramic-metal composite substrate comprising a ceramic substrate having opposite surfaces and a copper sheet having opposite faces, one face of which is directly bonded to one of the surfaces of the ceramic substrate and the other face of which comprises at least one mounting area and at least one electrode area, wherein the copper sheet has a Vickers hardness in the range of 40 kg/mm² to 100 kg/mm²; (b) at least one electrical element mounted on said at least one mounting area of the copper sheet, and (c) at least one bonding wire connecting said at least one electrical element with said at least one electrode area.

In accordance with a further aspect of the invention, a method of producing a bonded ceramic-copper composite substrate is provided comprising the steps of applying a copper member to a desired position on the surface of a ceramic substrate and heating the resulting assembly to a temperature below the melting point of copper but above the eutectic temperature of copper and oxygen to directly bond the copper member to the substrate, wherein said copper member is selected to have an initial hardness such that the Vickers hardness of said copper member after said heating step is in the range of 40 kg/mm² to 100 kg/mm².

In accordance with yet another aspect of the invention, a method of producing a circuit board is provided comprising the steps of (a) producing a bonded ceramic-metal composite substrate by applying a copper member to a desired position on the surface of a ceramic substrate and heating the resulting assembly to a temperature below the melting point of copper but above the eutectic temperature of copper and oxygen to directly bond the copper member to the substrate, said copper member having a free face comprising at least one mounting area and at least one electrode area, wherein said copper member is selected to have an initial hardness such that the Vickers hardness of said copper member after said heating step is in the range of 40 kg/mm² to 100 kg/mm²; (b) mounting at least one electrical element on said at least one mounting area of the copper sheet, and (c) electrically connecting a bonding wire to said at least one electrical element and said at least one electrode area.

In order that the invention may be more readily appreciated and carried into effect, embodiments thereof will now be described by way of example only, with reference to the accompanying drawings and in which:

5 Fig. 1 is a sectional view of the structure of a circuit board according to this invention, showing how a semiconductor element is mounted on the copper sheet, and

Fig. 2 is a plan view of a hollow made on a copper sheet with a diamond needle in a Vickers hardness test, showing a diagonal line Aa and a diagonal line Ab.

Detailed Description of Preferred Embodiments

10 There are several steps to obtain a circuit board. As seen in Fig.1, an electronic part 3 is mounted, for example by using solder, on a mounting area 2a of a copper sheet 2 bonded to a ceramic sheet 1. This electronic part 3 is electrically connected with one or more terminal electrode areas 2b of copper sheet 2 through aluminium wires 4. Finally, the circuit board may be covered with resin (not shown).

15 A copper sheet may be used which has already been punched out or stamped into a prescribed shape for the desired circuit. Alternatively, a continuous sheet of copper may subsequently be formed into conductor circuits by etching.

20 Many factors were considered as the cause why bonded ceramic-copper metal circuit boards did not work correctly or did not work at all. The present inventors discovered what the cause was. That is, the adhesion between the copper sheet and the aluminium bonding wire caused the trouble. The present inventors discovered that the wire exhibits excessive elongation around the bonded portion between the copper sheet and the wire, and that this excessive elongation may cause a break in the electrical connection between an electronic part 3 and the terminal electrode 2b of the copper sheet. The present inventors also discovered that there were some wires whose ends were buried in the copper sheet, and that 25 this burial may cause a break in the electrical connection between the electronic part 3 and the terminal electrode 2b of the copper sheet.

25 Ultrasonic bonding (a bonding method using ultrasonic energy) is used for bonding of thin wire. In early attempts to ultrasonically bond a thin aluminium wire to a bonded ceramic-copper circuit board substrate, it was found that the ultrasonic bonding did not work very well. As a result of a lot of experiments, the present inventors discovered that the hardness of the copper sheet was important to solve this problem. That is, it was discovered that a copper sheet whose Vickers hardness was less than 40 kg/mm² had a tendency to cause the problem that the wire is buried in the copper sheet by ultrasonic energy during ultrasonic bonding, permitting unnecessary elongation of the bonding wire in the region of the joint, causing rupture in the vicinity of the joint. It was also found that a copper sheet whose Vickers hardness was more than 100 kg/mm² had a tendency to cause the problem that the ultrasonic wave was attenuated during ultrasonic bonding, leading to a decrease in the adhesion. It is particularly preferred that the Vickers hardness of the copper sheet should be in the range of 60 kg/mm² to 80 kg/mm².

30 Copper sheets whose initial Vickers hardness was about 90 kg/mm² were first used to prepare bonded ceramic-copper substrates. The method of producing this kind of composite substrate, however, includes a step of heating to produce CuO at the bonding interface between the copper sheet and the ceramic substrate. Applicants discovered that the initial Vickers hardness of 90 kg/mm² was reduced to about 35 kg/mm² because of the heating step. It had apparently never been noticed before (1) that such a reduction in the hardness of a copper sheet occurs in the method of producing CuO at the bonding interface between the copper sheet and the ceramic sheet; (2) that the reduced hardness of a copper sheet permits 35 unnecessary elongation of the bonding wire in the region of the joint during ultrasonic bonding, and (3) that the reduced hardness of a copper sheet affects the operational reliability of the circuit board.

40 The hardness of the copper sheet can be controlled by regulating the annealing temperature and the annealing time in the process of finishing a copper ingot to a thin copper sheet, or by regulating the working ratio during rolling of the copper sheet. By choosing appropriate conditions, a copper member of the desired hardness can be obtained. Also, the hardness of the copper member that constitutes the starting 45 material is determined beforehand in accordance with the processing conditions, since the hardness of the copper member is affected by the heat treatment that is used to bond the copper member and ceramic substrate.

45 It is also desirable that the copper should be rolled so that the oxygen constituting the adhesive at least to the bonding face is contained in a proportion of 100 ppm to 3000 ppm with respect to the copper member that is used. A thickness of the copper sheet in the range of 0.25 mm to 0.6 mm is suitable.

50 Various types of ceramic substrates can be used as the ceramic substrate in this invention, for example sintered ceramic bodies of the oxide type, such as alumina or beryllia, or ceramic sintered bodies of the

non-oxide type, such as aluminium nitride, silicon nitride, titanium nitride, and silicon carbide.

If ceramic substrates of the non-oxide type are used, they are preferably employed after a prior oxidizing treatment of the bonding surface.

The ceramic substrates of this invention are manufactured, for example, as follows:

5 Specifically using a copper member having the prescribed hardness, a circuit pattern is produced by disposing a copper sheet preformed to the shape of the required circuit pattern, or a copper sheet in the form of a flat sheet, in contact with the ceramic substrate, bonding by heating to a temperature below the melting point of copper (1083 °C) but above the eutectic temperature of copper and oxygen (1065 °C), and if a flat sheet is used, etching the copper sheet as required to produce the required circuit pattern.

10 If a copper sheet containing oxygen is used as the copper sheet, the atmosphere during heat bonding is preferably an inert gas atmosphere. If a copper sheet not containing oxygen is used, an atmosphere containing 80 ppm to 3900 ppm of oxygen is preferable.

15 Vickers hardness can be determined as follows. First, a diamond needle having a weight of 100 g is placed on a copper sheet for 30 seconds. A diamond-shaped, hollow depression is thereby formed on the copper sheet. Vickers hardness can be calculated by using the following equations:

$$A = (Aa + Ab)/2$$

$$H = 1.8544 \cdot W/A^2$$

20 wherein

A represents the average length of the diagonal lines calculated with the length of diagonal line Aa and the length of diagonal line Ab (mm) [Fig.2]

W represents the needle weight (kg), and

H represents the Vickers hardness (kg/mm²).

25 Next, the invention will be described with reference to specific examples of preferred embodiments.

First of all, as shown in Fig.1, respective ceramic substrates were manufactured by respectively arranging both faces of a ceramic substrate 1, of which the main constituent is alumina, (containing 96 weight % alumina, and 4 weight % sintering adjuvant components) in contact with respective 0.3 mm thick copper sheets 2 having the initial Vickers hardnesses shown in the following Table, and effecting bonding 30 by heating for 10 minutes at 1075 °C in a nitrogen atmosphere. The subsequent Vickers hardnesses of the bonded copper sheets 2 in the resulting ceramic circuit substrates were respectively as shown in the following Table. Next, the respective silicon chips 3 were mounted on the bonded ceramic-copper substrates by soldering them to mounting portions 2a of copper sheets 2 of these ceramic circuit substrates, and respective Al wires 4 were bonded to the electrodes of semiconductor elements 3 and 35 terminal electrodes 2b of copper sheets 2 by the ultrasonic bonding method. The bonding strength of these Al wires 4 was then determined by a destructive test. The results obtained are shown in the following Table.

Table

40 Sample No.	Vickers Hardness (kg/mm ²)		Bonding Strength (kg/mm ²)	Mode of Failure
	Bonding Before	Bonding After		
45 1	100	40	11	Rupture in middle of wire
2	120	60	13	"
3	130	80	13	"
4	150	100	10	"
C1*	90	35	9	Rupture in area of joint
C2*	160	110	8	Exfoliation of wire

* Comparative Example

Note: When rupture occurred, the bonding strength was evaluated by the peel strength.

55 In the case of the comparative examples shown in the Table, ceramic circuit substrates and semiconductor modules were manufactured in the same way as in the embodiments of the invention, except that the initial Vickers hardnesses of the copper sheets were chosen so that the subsequent hardnesses of the bonded copper sheets were 35 kg/mm² and 110 kg/mm². The bonding strength of the aluminium wire was also measured by destructive testing in the same way.

As is clear from the results of the destructive tests reported in the Table, with the various ceramic circuit substrates of the embodiments according to the present invention, the bonding strength is high and rupture occurred at the middle of the Al wire. It can therefore be seen that firm and stable bonding of the Al wire had taken place. In contrast, in the case of the ceramic circuit substrates according to comparative example 1, in which the hardness of the copper sheet was low, the bonding strength was also low, and rupture occurred in the area of the joint, showing that unnecessary elongation of the Al wire had occurred. Also, in the case of the ceramic substrate of comparative example 2, in which the hardness of the copper circuit sheet was large, exfoliation of the wire occurred, clearly showing that the bonding strength was insufficient.

As described above, to produce a ceramic substrate according to this invention, the hardness of the copper sheet must be selected to have an appropriate value. As a result, a circuit substrate exhibiting improved, stable adhesion between the copper and an aluminium bonding wire can be produced by the action of ultrasonic waves. Consequently, products of excellent reliability are obtained when various electronic devices, such as semiconductor modules, are manufactured using such ceramic/copper substrates.

The foregoing description and examples have been set forth merely to illustrate the invention and are not intended to be limiting. Since modifications of the described embodiments incorporating the spirit and substance of the invention may occur to persons skilled in the art, the scope of the invention should be limited solely with reference to the appended claims and equivalents.

20

Claims

1. A bonded ceramic-metal composite substrate comprising a ceramic substrate having opposite surfaces and a previously annealed and/or rolled copper sheet having a face directly bonded to one of the surfaces of the ceramic substrate, wherein the copper sheet has a Vickers hardness after bonding in the range of 40 kg/mm² to 100 kg/mm².
2. A substrate as claimed in claim 1 in which the Vickers hardness of the copper sheet before bonding is 100 to 150 kg/mm².
3. A circuit board comprising:
 - (a) a bonded ceramic-metal composite substrate comprising a ceramic substrate having opposite surfaces and a copper sheet having opposite faces, one face of which is directly bonded to one of the surfaces of the ceramic substrate and the other face of which comprises at least one mounting area and at least one electrode area, wherein the copper sheet has a Vickers hardness after bonding in the range of 40 kg/mm² to 100 kg/mm²;
 - (b) at least one electrical element mounted on said at least one mounting area of the copper sheet, and
 - (c) at least one thin aluminium bonding wire connecting said at least one electrical element with said at least one electrode area.
4. A substrate according to claim 1 or 2 or a circuit board according to claim 3, wherein the copper has a Vickers hardness after bonding in the range of 60 kg/mm² to 80 kg/mm².
5. A substrate according to claim 1 or 2 or 4 or a circuit board according to claim 3 or 4, wherein the copper sheet has an oxygen content in the range of 100 to 3000 ppm.
6. A substrate according to any one of claims 1, 2, 4 or 5, or a circuit board according to any one of claims 3 to 5, wherein said at least one electrical element is soldered to said at least one mounting area.
7. A substrate according to any one of claims 1, 2, 4 or 6 or a circuit board according to any one of claims 3 to 6, wherein said copper sheet has a thickness in the range of 0.25 mm to 0.6 mm.

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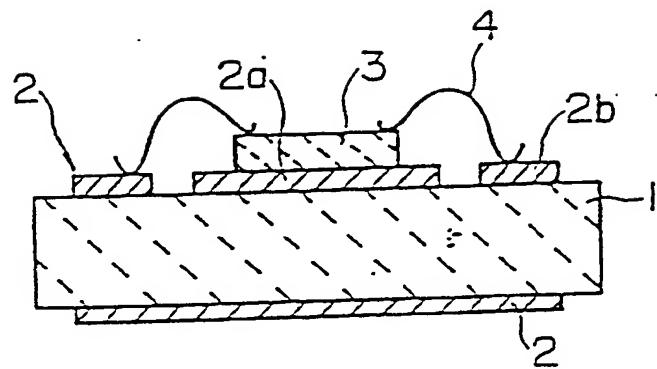


Fig. 1

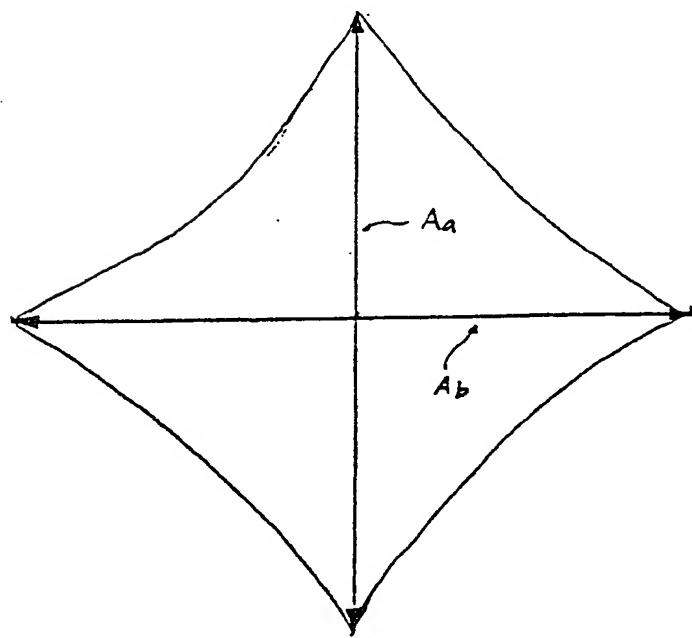


Fig. 2



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EUROPEAN SEARCH REPORT

Application Number

DOCUMENTS CONSIDERED TO BE RELEVANT			EP 93203467.1												
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 5)												
A	<u>EP - A - 0 219 254</u> (TOSHIBA) * Page 4, line 24 - page 5, line 7 * & JP-A-62-72 576 & US - A - 4 985 097 (MATSUMURA) + Column 3, line 58 - column 4, line 19; claim 2 + --	1, 3-7	H 01 L 21/86 H 01 L 49/02 H 05 K 1/09 H 05 K 3/32 H 05 K 7/02												
L		1, 3, 4													
A	<u>US - A - 4 591 401</u> (NEIDING) * Claim 1 *	1, 5													
D, A	<u>US - A - 3 994 430</u> (CUSANO) * Claims 24, 34, 35; fig. 9-14 *	5-7													
D, A	<u>US - A - 4 409 278</u> (JOCHYM) * Claims; columns 6-8 *	1, 3, 5-7	TECHNICAL FIELDS SEARCHED (Int. Cl. 5)												
A	<u>DE - A - 3 330 068</u> (DENKI KAGAKU KOGYO) * Fig.; pages 4, 7, 8 *	1, 3, 6	H 01 L H 05 K C 04 B B 23 K B 32 B												
<p>The present search report has been drawn up for all claims</p> <table border="1"> <tr> <td>Place of search VIENNA</td> <td>Date of completion of the search 16-02-1994</td> <td>Examiner HEINICH</td> </tr> <tr> <td colspan="3">CATEGORY OF CITED DOCUMENTS</td> </tr> <tr> <td colspan="3"> X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document </td> </tr> <tr> <td colspan="3"> T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document </td> </tr> </table>				Place of search VIENNA	Date of completion of the search 16-02-1994	Examiner HEINICH	CATEGORY OF CITED DOCUMENTS			X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document			T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document		
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